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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Yamazaki, et al                      Art Unit :                      Unknown  
Serial No.:                                              Examiner :                      Unknown  
Filed : June 27, 2001  
Title : SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD  
THEREOF

Commissioner for Patents  
Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Applicants submit the references listed on the attached form PTO-1449, copies of which are enclosed.

This filing is being made with the filing of the above-referenced application. No fee is required.

Each of the documents is either in the English language, or contains an English abstract, hence no concise explanation is necessary per rule 97(c).

Consideration of the foregoing and enclosures plus the return of a copy of the enclosed Form PTO-1449 with the Examiner's initials in the left column per MPEP 609 are earnestly solicited along with an early action on the merits.

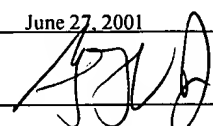
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Respectfully submitted,

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Substitute Form PTO-1449 (Modified)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 07977/280001/US5027	Application No.
<b>Information Disclosure Statement by Applicant</b> (Use several sheets if necessary) (37 CFR §1.98(b))		Applicant Yamazaki, et al	
		Filing Date June 27, 2001	Group Art Unit

1002 U.S. PTO  
 09/894950  
 06/27/01

U.S. Patent Documents							
Examiner Initial	Desig. ID	Patent Number	Issue Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						

Foreign Patent Documents or Published Foreign Patent Applications								
Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
	AI	07-130652	5/19/95	Japan			Abstract only	
	AJ	08-078329	3/22/96	Japan			Abstract only	
	AK	10-135468	5/22/98	Japan			Abstract only	
	AL	10-135469	5/22/98	Japan			Abstract only	
	AM							

Other Documents (include Author, Title, Date, and Place of Publication)		
Examiner Initial	Desig. ID	Document
	AN	Yamazaki, et al., U.S. Patent Application Serial No. 09/797,186 (pending), filed March 2, 2001, "Semiconductor Device and Manufacturing Method Thereof", pp. 70 specification, pp. 26 claims (claims 80), pp. 1 abstract, Figs. 1-24, one table. (Attorney Docket No. 12732/018001)
	AO	Yamazaki, et al., U.S. Patent Application Serial No. 09/809,646 (pending), filed March 16, 2001, "Semiconductor Display Device and Manufacturing Method Thereof", pp. 57 specification, pp. 11 claims (claims 25), pp. 1 abstract, Figs. 1-23. (Attorney Docket No. 12732/021001)
	AP	
	AQ	

Examiner Signature	Date Considered
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	